



YC1026

Bluetooth 3.0 BR/EDR+BT5.0+2.4GHz-Proprietary

Datasheet

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Revision History

Version	Date	Author	Description
preliminary	2015-7-30	M.W.	Initial version
V1.0	2015-10-22	M.W.	Modify Pinout
V1.1	2015-10-29	M.W.	Add info. for VDDLPM pin
V1.2	2015-11-16	F.K.	Add Power consumption, Security, Bluetooth Stack, MFI information and Application Schematic.
V1.3	2015-12-9	M.W.	GPIO2 & GPIO24~26 should NOT be connected to GND when unused
V1.33	2016-3-17	M.W	Clecial error fix;Modify pinout.
V1.34	2016-5-27	F.K.	Updated application schematic.
V1.35	2016-5-30	M.W	Add crystal recommendations
V1.36	2018-6-23	J.Z	Update the application schematic
V1.37	2018-9-23	J.Z	Update Package Physical Dimension
V1.38	2020-5-06	J.Z	Upgrade the version from BT3.0 to BT5.0

General Description

The YC1026 is a very low power, high performance and highly integrated Bluetooth 3.0 BR/EDR+BT5.0+2.4G Proprietary triple-mode solution, designed for operation over the 2400MHz to 2483.5Mhz ISM frequency band.

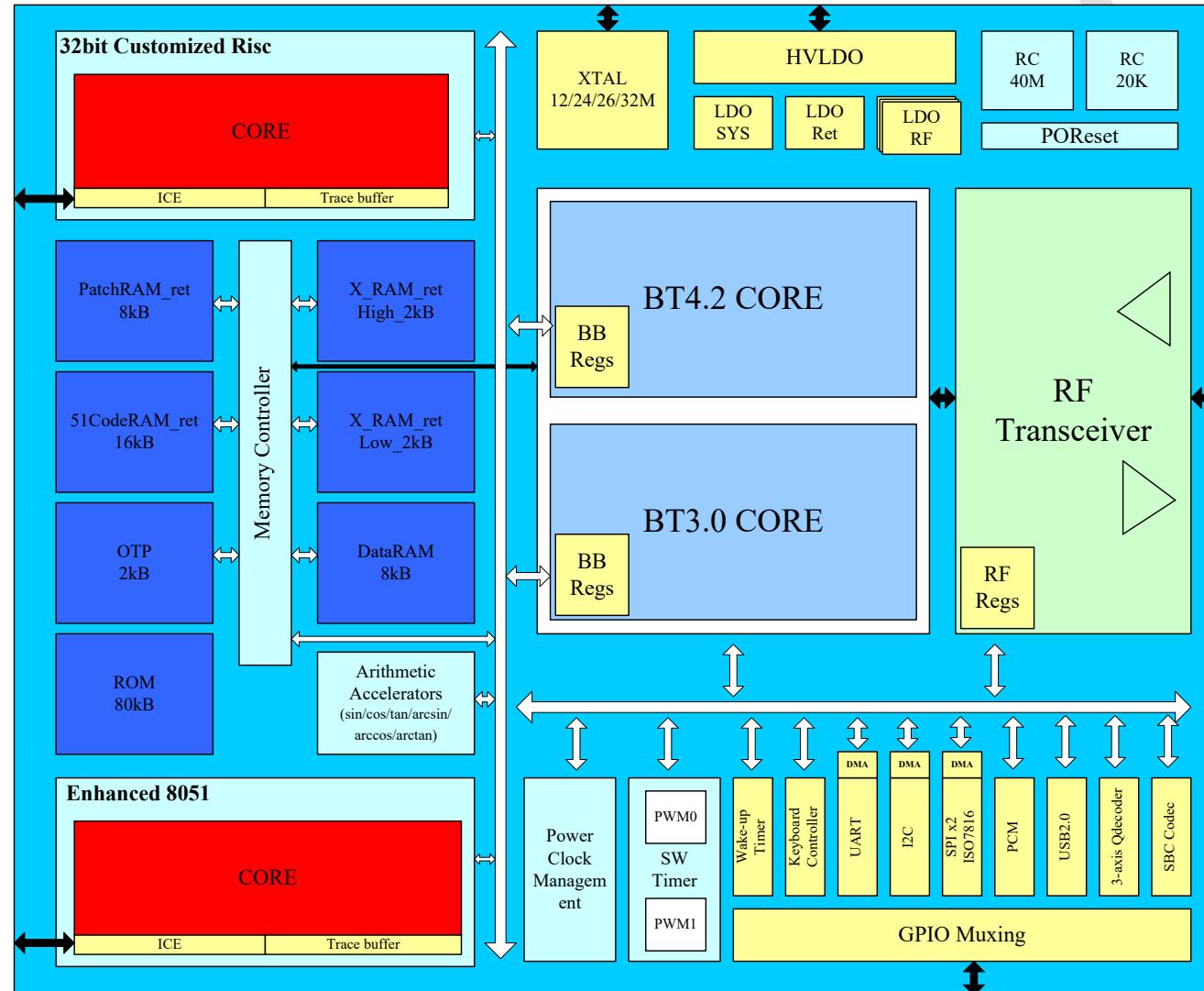
YC1026 is manufactured using advanced 55nm CMOS low leakage process, which offers highest integration, lowest power consumption, lowest leakage current and reduced BOM cost while simplifying the overall system design. Rich peripherals including an 8 channel general purpose ADC, power-on-reset (POR), Arithmetic Accelerators, USB2.0, 3axis Q-decoder, ISO7816, SPI/I2C and up to 32 GPIOs, which further reduce overall system cost and size.

YC1026 operates with a power supply range from 1.8 to 5.5V and has very low power consumption in both Tx and Rx modes, enabling long lifetimes in battery-operated systems while maintaining excellent RF performance. The device can enter an ultra low power sleep mode in which the registers and retention memory content are retained while low power Oscillator and sleep timer is ON. Different package from QFN4x4_32L (YC1028, upto 17 GPIOs, for mouse application) to QFN7x7_56L (YC1026, upto 32 GPIOs, for keyboard application) is available.

Key Features

- Bluetooth 3.0
BR/EDR+BT5.0+2.4GHz-Proprietary triple-mode
RF SOC
- Very Low Power Consumption
 - 10nA shut down mode (external interrupts)
 - 620nA sleep mode (32kHz RC OSC, sleep timer and register ON)
 - 2uA retention mode (32kHz RC OSC, sleep timer, 2k retention memory and register ON)
 - Rx peak current @3V (ideal DCDC)
 - 6.75mA in BLE/2.4G mode
 - 7.25mA in 3.0(EDR) mode
 - Tx peak current @3V (-2dBm, ideal DCDC)
 - 16.5mA in BLE/2.4G mode
 - 17mA in 3.0(EDR) mode
 - Rx peak current w/o DCDC
 - 16mA in BLE/2.4G mode
 - 17mA in 3.0(EDR) mode
 - Tx peak current w/o DCDC @ -2dBm
 - 22mA in BLE/2.4G mode
 - 23mA in 3.0(EDR) mode
 - <25uA avg, 500ms sniff hold connection
- 2.4GHz Transceiver
 - Single-end RFIO
 - -93dBm in BLE mode
 - support 250kbps, 1/2/3Mbps data rates
 - Tx Power upto +6dBm
- Oscillators
 - 16M/24M/32M XTAL supported (default 24M)
 - 50M RC oscillator
 - Low Jitter 32K RC oscillator
- Digital Architecture
 - 32bit-Risc Core for link management
 - 80kB code ROM
 - 8kB code RAM
 - All RAMs can be set to retention mode
 - Arithmetic Accelerators [Accuracy : (sign, 15b.16b)]
 - sin/cos/tan/sin⁻¹/cos⁻¹/tan⁻¹/multi/div/sqrt
- Analog Peripherals
 - 8 channel ADC with 10 bit accuracy/3Msps
- Digital Peripherals
 - USB 2.0 full speed (12Mbps)
 - Two-wire Master (I2C compatible), upto 400kbps;
 - AES128 HW encryption
 - LED drive capability
 - PWM
 - 20x8 keyscan
 - 3 axis Q-decoder

Block Diagram



Electrical Specifications

Name	Parameter (Condition)	Min	Typ	Max	Unit	Comment
Power Supplies						
HVIN	Voltage Input, typically 1uF decouple cap	3.1	4.2	5.5	V	(1)
HVOUT	Voltage Output, typically 1uF decouple cap, maximum 50mA load capability	2.75	2.85	2.95	V	
IQ_HV	Quiescent Current of high voltage LDO		750		nA	
VIN	Voltage Input, typically 1uF decouple cap	1.5		3.6	V	
VINPA	Voltage Input, typically 5pF decouple cap	1.5		3.6	V	(2)
VINLPM	Voltage Input	1.8		3.6	V	(3)
VIO	Voltage Input	1.7		3.6	V	(4)
DVDD	Voltage Output, typically 1uF decouple cap	1.1	1.2	1.3	V	
VDDLPM	Voltage Output, typically 100nF decouple cap	1.1	1.2	1.3	V	
Temperature						
TEMP	Temperature	-20		+85	°C	
Digital Input Pin						
VIH	High Level	VIO-0.3		VIO+0.3	V	
VIL	Low Level	VSS		VSS+0.3	V	
Digital Output Pin						
VOH	High Level	VIO-0.3		VIO+0.3	V	(5)
VOL	Low Level	VSS		VSS+0.3	V	
Current Consumption						
IVDD	Shut down mode, can only be waked up by wake-up pin.		10		nA	
IVDD	Retention mode (LPO, no retention RAM, POR, sleep timer, I/O interrupts ON), can be waked up by sleep timer & any GPIO		0.70		uA	(6)
IVDD	Retention mode (LPO, 2kB retention RAM, POR, sleep timer, I/O interrupts ON), can be waked up by sleep timer & any GPIO		1.25		uA	
IVDD	RX mode, BLE & 2.4G mode, 100% ON (with ideal DCDC @3V)		6.75		mA	(7)
IVDD	RX mode, EDR mode, 100% ON (with ideal DCDC @3V)		7.25		mA	
IVDD	TX mode, BLE & 2.4G mode, 100% ON (with ideal DCDC @3V)		16		mA	(8)
IVDD	TX mode, EDR mode, 100% ON (with ideal DCDC @3V)		17		mA	
IVDD	Average Current, 500ms sniff, hold connection			25	uA	
Normal RF Condition						
FOP	Operating Frequency	2400		2480	MHz	

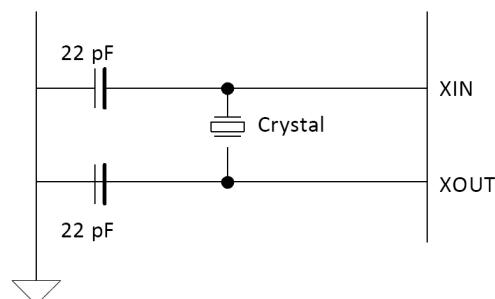
FXTAL	Crystal Frequency	12	24	32		(9)
Transmitter Characteristics						
PRF	RF output power	-20	0	6	dBm	
CD	Carrier Drift Rate		5		kHz/50us	
PRF1	Out of band emission 2 MHz (GFSK)		-40		dBm	
PRF2	Out of band emission 3 MHz (GFSK)		-48		dBm	
BW	20dB bandwidth		0.9		MHz	
EVM	Modulation Accuracy, RMS DEVM ($\pi/4$ DQPSK)		7	20	%	
	Modulation Accuracy, RMS DEVM (8PSK)		7	13	%	
	Modulation Accuracy, 99% DEVM ($\pi/4$ DQPSK)		14	30	%	
	Modulation Accuracy, 99% DEVM (8PSK)		14	20	%	
	Modulation Accuracy, Peak DEVM ($\pi/4$ DQPSK & 8PSK)		18	35	%	
	Modulation Accuracy, Peak DEVM (8PSK)		18	25	%	
PRF1	Out of band emission 2 MHz ($\pi/4$ DQPSK & 8PSK)		-30	-20		
PRF2	Out of band emission 3 MHz ($\pi/4$ DQPSK & 8PSK)		-42	-40		
Receiver Characteristics						
	BT5.0 (BLE)					
SEN	High Gain mode, Sensitivity @0.1%		-93		dBm	
SEN	Standard Gain mode, Sensitivity @0.1%		-87		dBm	
MaxIn	Maximum Input Power		0		dBm	
C/ICO	Co-channel C/I, Basic Rate, GFSK		7		dB	
C/I1ST	ACS C/I 1MHz, Basic Rate, GFSK		5.5	7	dB	
C/I2ND	ACS C/I 2MHz, Basic Rate, GFSK		-36	-34	dB	
C/I3RD	ACS C/I 3MHz, Basic Rate, GFSK		-43		dB	
C/I1STI	ACS C/I Image channel, Basic Rate, GFSK		-34		dB	
C/I2NDI	C/I 1 MHz adjacent to image channel, Basic Rate, GFSK		-28		dB	
	BT3.0 (BR & EDR)					
SEN	Basic Rate, GFSK, BER<0.1%, Dirty Tx on		-90		dBm	
SEN	EDR, $\pi/4$ DQPSK, BER<0.01%, Dirty Tx on		-91		dBm	
SEN	EDR, 8PSK, BER<0.01%, Dirty Tx on		-83		dBm	
MaxIn	Maximum Input Power		0		dBm	
C/ICO	Co-channel C/I, EDR, $\pi/4$ DQPSK		10.5		dB	
C/I1ST	ACS C/I 1MHz, EDR, $\pi/4$ DQPSK		-8		dB	
C/I2ND	ACS C/I 2MHz, EDR, $\pi/4$ DQPSK				dB	
C/I3RD	ACS C/I 3MHz, EDR, $\pi/4$ DQPSK		-54		dB	
C/I1STI	ACS C/I Image channel, EDR, $\pi/4$ DQPSK		-27		dB	
C/I2NDI	C/I 1 MHz adjacent to image channel, EDR, π		-43		dB	

	/4 DQPSK					
C/ICO	Co-channel C/I, EDR, 8PSK		20		dB	
C/I1ST	ACS C/I 1MHz, EDR, 8PSK		0		dB	
C/I2ND	ACS C/I 2MHz, EDR, 8PSK		-20		dB	
C/I3RD	ACS C/I 3MHz, EDR, 8PSK		-45		dB	
C/I1STI	ACS C/I Image channel, EDR, 8PSK		-18		dB	
C/I2NDI	C/I 1 MHz adjacent to image channel, EDR, 8PSK		-33		dB	

- (1) HVIN & HVOOUT are input & output of a high voltage LDO which is integrated in YC1026, input voltage range from 3.1~5.5V, and maximum load capability upto 50mA. Typically used in Li_BAT (3.2~4.2V) or USB_Power(4.5~5.5V) applications. If input voltage is not lower than 3.6V, HVIN & HVOOUT should be left unconnected and YC1026 should be powered by VIN/VINLPM/VINPA directly.
- (2) If RF output power should be larger than -4dBm, VINPA should be larger than 2.5V.
- (3) VINLPM should always be powered ON in all working cycles.
- (4) VIO should always be powered ON in all working cycles.
- (5) Drive capability of GPIO[6:7] & GPIO[18:22] is up to 30mA, other GPIO's drive capability is 10mA
- (6) By default, 2kB retention memory is ON in retention mode. Up to 4kB retentionable X_memory available at the cost of extra 600nA retention mode current. Besides, 16kB 51-code memory is also retentionable at the cost of extra 1.6uA retention mode current.
- (7) Result based on standard gain mode
- (8) Result based on -2dBm Pout
- (9) 12M, 16M, 24M, 26M, 32M crystal supported, 24M by default

Crystal Oscillator

The crystal oscillator requires a crystal with an accuracy of ± 30 ppm as defined by the Bluetooth specification. Two external load capacitors in the range of 5 pF to 30 pF are required to work with the crystal oscillator. The selection of the load capacitors is crystal dependent. The recommended crystal specification shows below.



Recommended Oscillator Configuration — 20 pF Load Crystal

Reference Crystal Electrical Specifications

Name	Parameter (Condition)	Min	Typ	Max	Unit	Comment
Frequency			24		MHz	
Oscillation mode			Fundamental			
Frequency tolerance	@25°C		±10	±30	ppm	
Tolerance stability over temp	@0°C to +70°C		±10	±30	ppm	
Load capacitance			20		pF	
Operating temperature range		-20		+70	degree	
Drive Level			100		uW	

Power consumption

W/O DC-DC	Parameter	Average Current	Unit
Sleep	/	700	nA
Sniff	500ms interval	21.99	uA
Discoverable	ADV interval: 640ms Scan interval: 1280ms Scan window: 11.25ms	138.66	uA

With DC-DC	Parameter	Average Current	Unit
Sleep	/	700	nA
Sniff	Sniff Interval:500ms	17.92	uA
Discoverable	ADV interval: 640ms Scan interval: 1280ms Scan window: 11.25ms	89.5	uA

Bluetooth Security

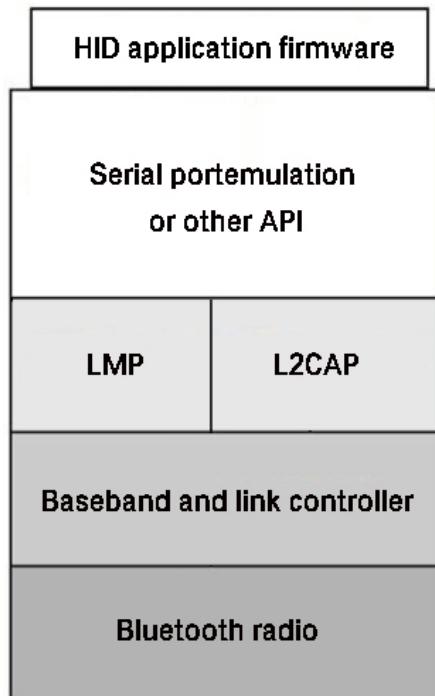
1. Pairing
 - Pin Code
2. Security Simple Pairing
 - Just Work(No input)
 - Keyboard
 - DisplayYesNo

MFi

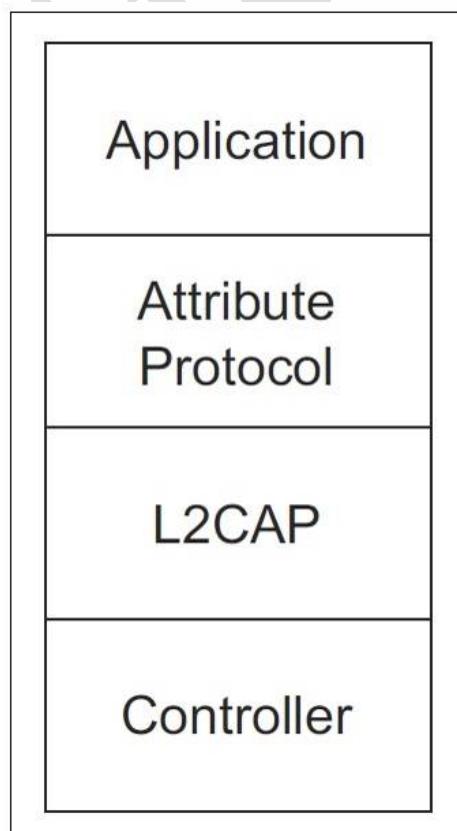
Support Apple's MFi authentication and iAP1/iAP2 protocols.

Bluetooth Stack

1. Serial Port Profile

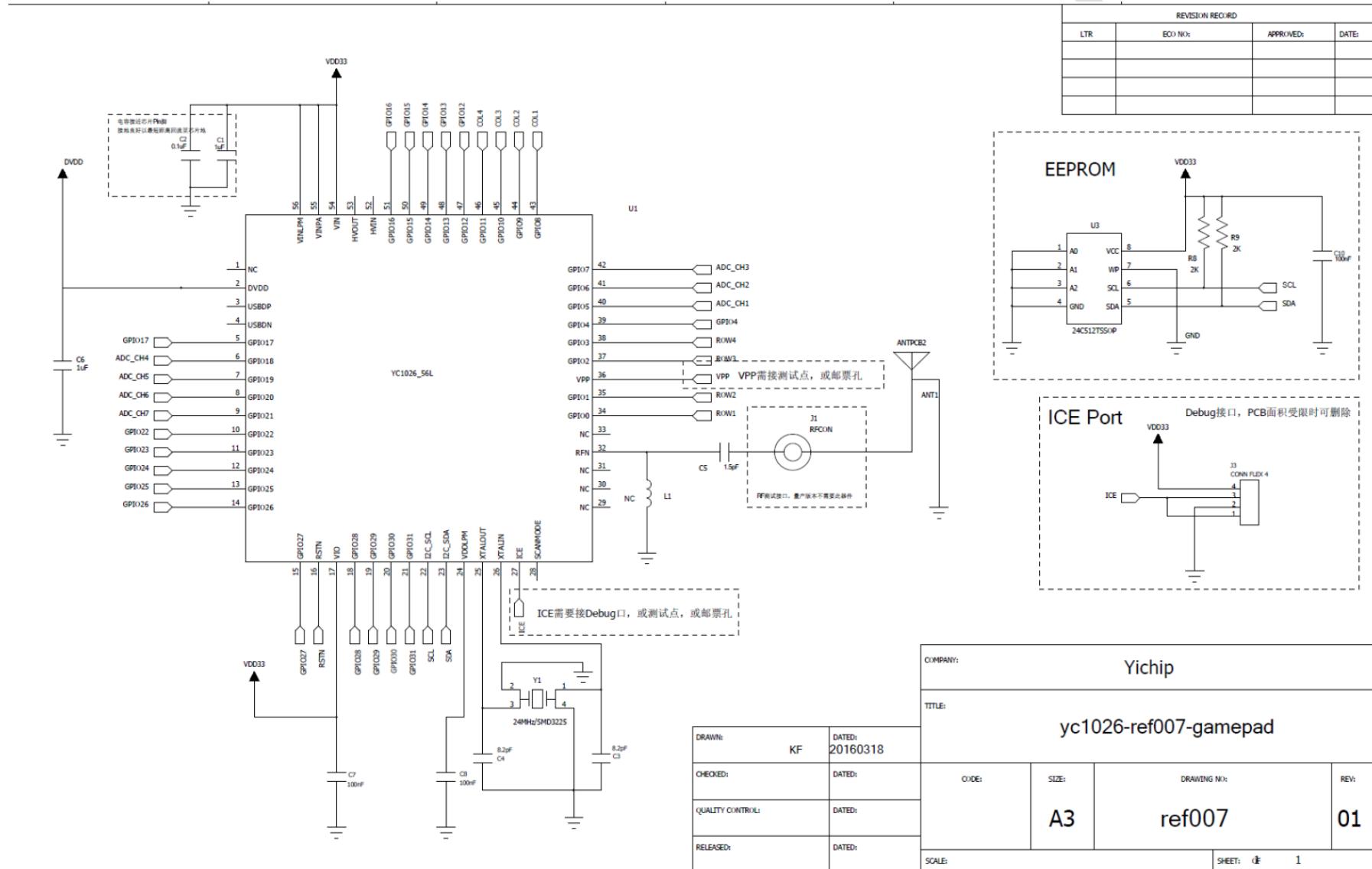


2. Generic Attribute Profile

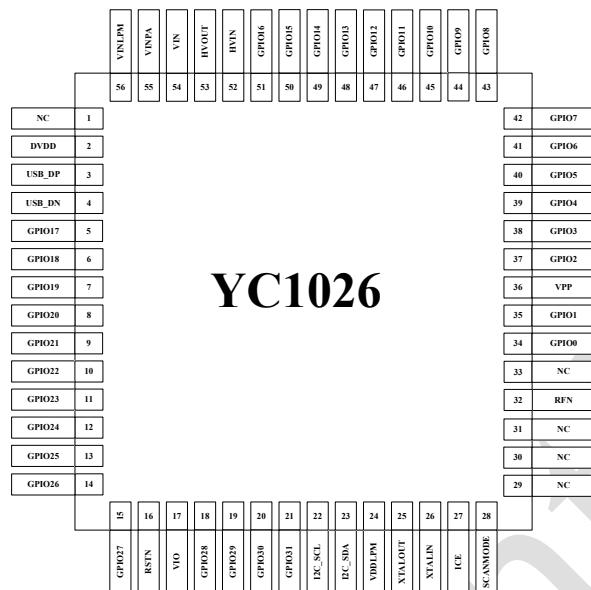


Application Schematic

1. Bluetooth Keyboard application



Package Information



7x7 56L	4x4 32L	Pin Name	Type	Function Description
56	32	VINLPM	Power_I	Power supply in, 1.8~3.6V
2	1	DVDD	Power_O	internal LDO output, 1.2V. Need an external bypass cap here 1uF
3	2	USB_DP	Dig_I	USB port
4	3	USB_DN	Dig_I	USB port
5	/	GPIO17	Dig_IO	pls check "sheet: GPIO_Muxing"
6	/	GPIO18	Dig_IO	pls check "sheet: GPIO_Muxing"
7	/	GPIO19	Dig_IO	pls check "sheet: GPIO_Muxing"
8	/	GPIO20	Dig_IO	pls check "sheet: GPIO_Muxing"
9	/	GPIO21	Dig_IO	pls check "sheet: GPIO_Muxing"
10	/	GPIO22	Dig_IO	pls check "sheet: GPIO_Muxing"
11	4	GPIO23	Dig_IO	pls check "sheet: GPIO_Muxing"
12	5	GPIO24	Dig_IO	pls check "sheet: GPIO_Muxing"
13	6	GPIO25	Dig_IO	pls check "sheet: GPIO_Muxing"
14	7	GPIO26	Dig_IO	pls check "sheet: GPIO_Muxing"
15	8	GPIO27	Dig_IO	pls check "sheet: GPIO_Muxing"
16	9	RSTN	Dig_I	Gloable reset, active low. OR gated with internal POR. NC if not needed.
17	10	VIO	Power_I	I/O Power, 1.8~3.6V, 100nF//10pF bypass cap
18	11	GPIO28	Dig_IO	pls check "sheet: GPIO_Muxing"
19	/	GPIO29	Dig_IO	pls check "sheet: GPIO_Muxing"
20	/	GPIO30	Dig_IO	pls check "sheet: GPIO_Muxing"
21	/	GPIO31	Dig_IO	pls check "sheet: GPIO_Muxing"

22	12	I2C_SCL	Dig_IO	pls check "sheet: GPIO_Muxing"
23	13	I2C_SDA	Dig_IO	pls check "sheet: GPIO_Muxing"
24	14	VDDLPM	Power_O	internal LDO output, 1.2V. Need an external bypass cap here 100nF
25	15	XTALOUT	Ana_O	XTAL port
26	16	XTALIN	Ana_I	XTAL port, or external CLK in
27	17	ICE	Dig_IO	debug port, Tx & Rx
28	18	SCANMODE	Dig_I	SCAN Test enable pin
32	19	RFN	RF Port	ANT port
/	/	RFP	RF Port	ANT port
34	/	GPIO0	Dig_IO	pls check "sheet: GPIO_Muxing"
35	/	GPIO1	Dig_IO	pls check "sheet: GPIO_Muxing"
36	20	VPP	Power_I	OTP Program Power, 6.5V
37	21	GPIO2	Dig_IO	pls check "sheet: GPIO_Muxing"
38	22	GPIO3	Dig_IO	pls check "sheet: GPIO_Muxing"
39	23	GPIO4	Dig_IO	pls check "sheet: GPIO_Muxing"
40	24	GPIO5	Dig_IO	pls check "sheet: GPIO_Muxing"
41	25	GPIO6	Dig_IO	pls check "sheet: GPIO_Muxing"
42	26	GPIO7	Dig_IO	pls check "sheet: GPIO_Muxing"
43	27	GPIO8	Dig_IO	pls check "sheet: GPIO_Muxing"
44	/	GPIO9	Dig_IO	pls check "sheet: GPIO_Muxing"
45	/	GPIO10	Dig_IO	pls check "sheet: GPIO_Muxing"
46	/	GPIO11	Dig_IO	pls check "sheet: GPIO_Muxing"
47	/	GPIO12	Dig_IO	pls check "sheet: GPIO_Muxing"
48	/	GPIO13	Dig_IO	pls check "sheet: GPIO_Muxing"
49	/	GPIO14	Dig_IO	pls check "sheet: GPIO_Muxing"
50	/	GPIO15	Dig_IO	pls check "sheet: GPIO_Muxing"
51	/	GPIO16	Dig_IO	pls check "sheet: GPIO_Muxing"
52	28	HVIN	Power_I	HV LDO input, 3~5.5V, 4.7uF bypass cap
53	29	HVOUT	Power_O	HV LDO output, 2.85V. Bypass cap need here, 1uF
54	30	VIN	Power_I	Power supply in, 1.8~3.6V, 100nF//5pF bypass cap
55	31	VINPA	Power_I	Tx_PA's power supply, 1.8~3.6V,5pF bypass cap

Note: Most GPIOs are by default configured to input status after power-on reset, except for GPIO2 & GPIO24/25/26 which are in output status. If a GPIO is not used as well as it is not configured to output, it can be connected to GND. But GPIO2 & GPIO24/25/26 MUST NOT be connect to GND at any time.

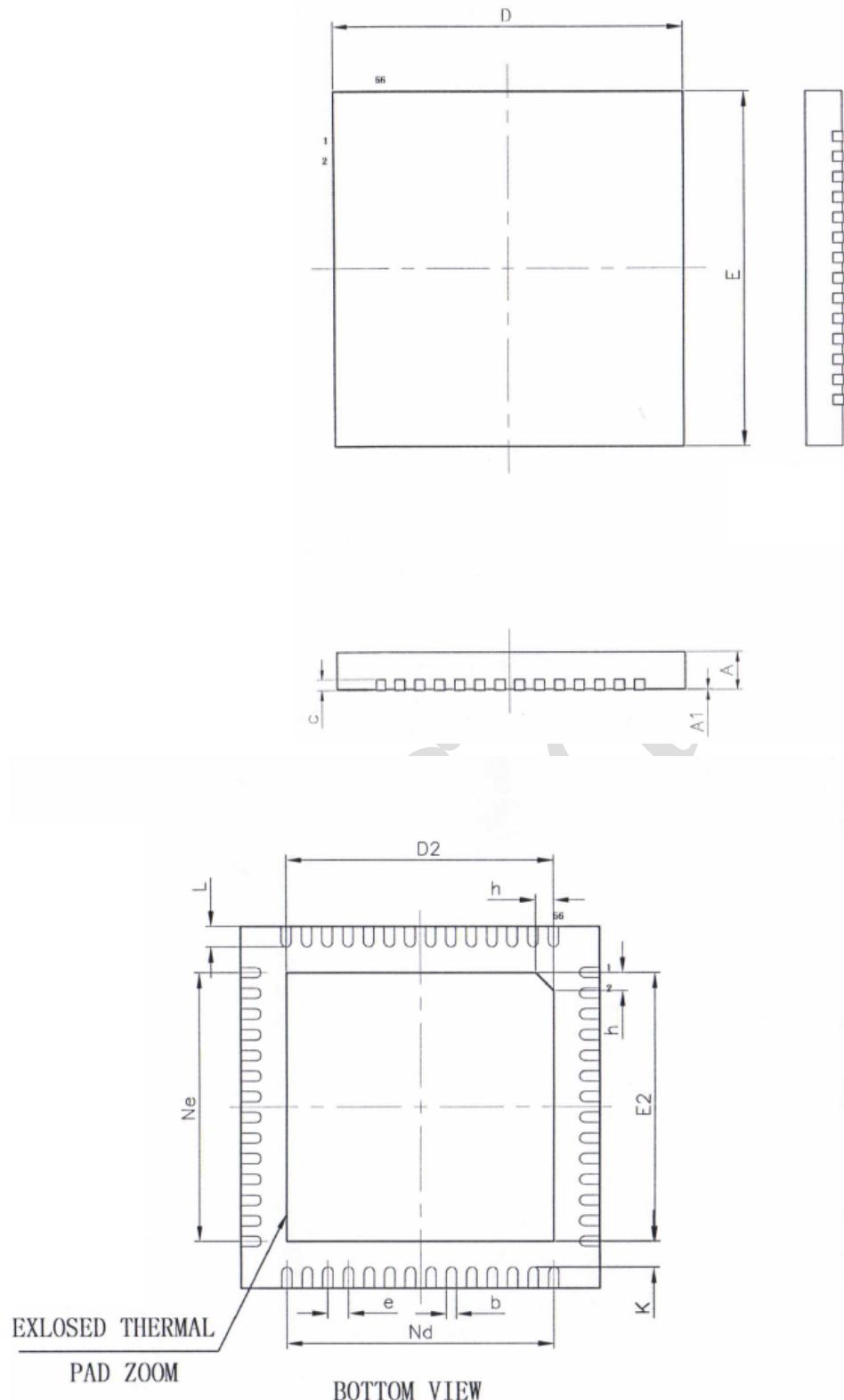
GPIO Muxing Table

GPIOs	Function1	Function2	Function-Analog	Keyscan
GPIO[0]	XA	/	/	row 1
GPIO[1]	XB	/	/	row 2
GPIO[2]	/	EXEN	/	row 3
GPIO[3]	/	PWM OUT0	/	row 4
GPIO[4]	PWM OUT0	/	/	row 5
GPIO[5]	PWM OUT1	/	adc_channel1	row 6

GPIO[6]	/	PWM OUT0	adc_channel2	row 7
GPIO[7]	/	PWM OUT0	adc_channel3	row 8
GPIO[8]	/	/	wakeup	col 1
GPIO[9]	SPIMISO-B	/	/	col 2
GPIO[10]	SPICS-B	/	/	col 3
GPIO[11]	SPICLK-B	/	/	col 4
GPIO[12]	SPIMOSI-B	/	/	col 5
GPIO[13]	/	/	CMP-	col 6
GPIO[14]	/	/	CMP+	col 7
GPIO[15]	/	/	/	/
GPIO[16]	/	/	/	/
GPIO[17]	SIP_EEPROM_WP	/	/	col 8
GPIO[18]	PWM OUT0	/	adc_channel4	col 9
GPIO[19]	PWM OUT0	/	adc_channel6	col 10
GPIO[20]	PWM OUT0	/	adc_channel6	col 11
GPIO[21]	PWM OUT0	/	adc_channel7	col 12
GPIO[22]	PWM OUT0	/	adc_channel8	col 13
GPIO[23]	SPIMISO	PCM DOUT	/	col 14
GPIO[24]	SPICS	PCM DIN	/	col 15
GPIO[25]	SPICLK	PCM CLK	/	col 16
GPIO[26]	SPIMOSI	PCM SYNC	/	col 17
GPIO[27]	YA	CLKREQ_IN	/	col 18
GPIO[28]	YB	CLKREQ_OUT	/	col 19
GPIO[29]	ZA	/	/	col 20
GPIO[30]	ZB	/	/	/
GPIO[31]	/	/	/	/

Note: Drive capability of GPIO[6:7] & GPIO[18:22] is up to 30mA, other GPIO's drive capability is 10mA

Package Physical Dimension (QFN7x7_56L)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	—	0.02	0.05
b	0.15	0.20	0.25
c	0.18	0.20	0.25
D	6.90	7.00	7.10
D2	5.10	5.20	5.30
e	0.40BSC		
Nd	5.20BSC		
Ne	5.20BSC		
E	6.90	7.00	7.10
E2	5.10	5.20	5.30
K	0.20	—	—
L	0.35	0.40	0.45
h	0.30	0.35	0.40
L/F载体尺寸 (mil)	217*217		